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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

O I P E  
SEP 08 2005  
In Re Patent Application of:  
Justin K. Brask et al.  
Application No.: 10/828,958  
Filed: April 20, 2004  
For: A METHOD FOR MAKING A  
SEMICONDUCTOR DEVICE HAVING A  
HIGH-K GATE DIELECTRIC LAYER AND  
A METAL GATE ELECTRODE)

Examiner: Unknown

Art Unit: 2812

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to the Commissioner for Patents, PO Box 1450, Alexandria, Virginia 22313-1450

on September 6, 2005  
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Sir:

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Respectfully submitted,  
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